

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Bryan D. Sheffield, et al.

Art Unit: 2185

Serial No.: 10/663,144

Examiner: Daniel Y. Kim

Filed: September 16, 2003

Docket: TI-35286

For: METHOD OF OPERATING A MEMORY AT HIGH SPEED USING A CYCLE  
READY STATUS OUTPUT SIGNAL

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Commissioner:

**AMENDMENT UNDER C.F.R. § 1.116**

In response to the Official Action in this case mailed October 19, 2006, please enter the following: